



Yuchen Jin

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Huazhong University of
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Wuhan, Hubei

Yuchen Jin

Speech Researcher

About Me I am a student from Huazhong University of Science & Technology (HUST). During these year I think I could be proved to be serious on studying and hard-working. I want to participate in the research group in SLT, firstly, because I find myself interested in the filed of signal processing. Another reason is my personal interests in digital music and songs. I am looking forward to make progress during the researches with the help of your group.

Education

2013 - 2016, Huazhong University of Science & Technology
Electronic Information

Experience

2014 - 2016, Study in Signal Processing and Information Networking in Communications in Wuhan National Laboratory for Optoelectronics, HUST

Learning:

- Paper writing skills.
- Resource scheduling optimization for cloud computing in data link layer (5G communication).
- Video structure of MPEG4.

Oct 2016, Field trip in ZTE

Attend in the training about optical communication and LTE.

Communication Skills

2014, Oral Presentation, Leader of the group design in Computer Technology

Led the group and presented an user-interactive calculator designed by C program.

2014, Oral Presentation, Leader of the group design in Electronic Circuit Design and Experiment (Analog)

Led the group and presented a radio receiver.

2015, Oral Presentation, Leader of the group design in Signal and System

Led the group and presented a series of researches in speech including: (1) a Hi-Fi digital music producer (2) a frequency equalizer (3) an electronic reverberation controller (3) a speech speed changer based on WSOLA (4) a simple DCT zipper of digital volume.

2015, Oral Presentation, Leader of the group design in Electronic Circuit Design and Experiment (Digital)

Led the group and presented a digital signal generator on Xilinx FPGA.

2015, Oral Presentation, Leader of the group design in Software Disign

Led the group and presented a TCP-IP file exchanger on Qt 5.



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Communication Skills

2016, *Oral Presentation, Leader of the group design in Altera FPGA Design*

Led the group and presented a video game on Altera FPGA corresponding with VGA, SD Card and PS Keyboard.

Software and Hardware Development Skills

Software Programming

- C
- C++
- Matlab
- Ruby

Hardware Programming

- Verilog
- IP core design
- Soft-core based on C

Tex

- \LaTeX

Awards

2014, *National Scholarship*

Awarded to the top student in a school year.

2014, *Sanhao Student Scholarship, HUST*

Awarded to the top student in a school year.

2015, *Study Scholarship, HUST*

Awarded to the student with a good academic performance in a school year.

Certifications

CET-4

599

CET-6

535

National Computer Rank Examination-4

Academic records / Credits

Freshman	Sophomore	Junior
89.56/57.5	89.22/54.5	86.74/44.0

Average: 88.60/146.0

Interests

Professional

Frequency analysis, resource scheduling, cloud computing, multi-media.

Personal

Violin, reading books (epic, mystery novel, science fiction and historical fiction).